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## HIGH-SPEED DSP THEORY. APPLICATION AND EXPERIMENTAL COURSE



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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Paperback. Pages Number: 309 Publisher: Tsinghua University Press Pub. Date :2009-05-01. U.S. Texas Instruments (TI) has digital signal processor (DSP) since the early 1980s inception. with its unique architecture. flexible resource allocation. a variety of digital signal processing to achieve fast and precise control algorithm of the outstanding advantages as the world s highest market share mainstream products. High-speed DSP theory. application and Experimental Course to TI s three main push one C2XXX DSP series chip as the core point. systematic exposition of its structure and composition theory. engineering applications and validation experiments. and with some thought and practice problem. High-speed DSP theory. application and Experimental Course for TIDSP research and development in electrical engineering and electronic information disciplines such as advanced undergraduate or graduate, and professional engineers and other industry related personnel information. Contents: Introduction Chapter 1 Principles article 1.1 Overview 1.2 digital signal processing systems digital signal processing technology way to 1.2.1 What is a DSP 1.2.2 digital signal processing chip implementation 1.3DSP development status and trends 1.3.1 What is a DSP chip 1.3.2DSP. DSP chips produced with DSPS 1.3.3DSP chip manufacturers 1.3.4DSP with major trends 1.4DSP chip chip-chip classification 1.5DSP the main features of the application 1.6TMS320C2XXDSP main 1.6.1TMS320 series 1.6.2TMS320C2XX Features thinking questions in Chapter 2 of this chapter C2XX hardware components and architecture of the hardware components 2.2C2XX 2.1 Overview 2.2.1 Central Processing Unit (CPU) 2.2.2 Memory and IFour Satisfaction guaranteed, or money back.



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